

between all plates of opposing electrical charge. As described in the specification and as now included in the claims, such a layer allows the parallel electrical connection of the stacked capacitor array in accordance with the present invention.

CONCLUSION

In view of the foregoing comments, Applicants respectfully request withdrawal of the current ground of rejection and the issuance of a formal Notice of Allowance. The Examiner is invited to telephone the undersigned at his convenience should only minor issues remain after consideration of this response in order to permit early resolution of the same.

Respectfully submitted,

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1/9/02
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